

IN THE CLAIMS:

Please amend claims 1, 2, 4, 7, 8 and 11 as follows:

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1. (Amended) A method of sequentially connecting one or more testing devices to I/O ports of a DUT through a switching network, so as to execute a predetermined testing procedure associated with the DUT, comprising:

generating a switching network map defining one or more connections within the switching network necessary to implement each of a plurality of electrical paths from an input of the switching network to an output of the switching network, wherein each of the plurality of electrical paths is representative of a connection of one of the testing devices to one of the I/O ports of the DUT;

receiving one or more commands, wherein each of the commands uniquely specifies an electrical path connecting a particular testing device to a particular I/O port of the DUT; and,

for each of the one or more commands, comparing the command to the switching network map so as to identify a corresponding electrical path through the switching network, and implementing the corresponding electrical path associated with the command through the switching network; and,

sequentially implementing the electrical paths corresponding to the one or more commands in a predetermined order.

2. (Amended) A method according to claim 1, further including assigning a unique path name to each of the electrical paths, such that each command specifies a particular electrical path via the path name.

3. A method according to claim 1, further including sequentially implementing the electrical paths associated with the one or more commands through the switching network in an order corresponding to a chronological order of the one or more commands.

4. (Amended) A method according to claim 1, wherein sequentially implementing the electrical paths further includes opening and closing selected switching devices within the switching network.

5. A method according to claim 1, further including programming a computer system to issue the commands in the predetermined order.

6. A method according to claim 1, further including connecting one or more testing devices to multiple DUT sites via the switching network.

7. (Amended) A method according to claim 1, further including associating each of the electrical paths with a name that is (i) descriptive of the path and (ii) related to the DUT.

8. (Amended) A system for sequentially connecting one or more testing devices to I/O ports of a DUT through a switching network, so as to execute a predetermined testing procedure associated with the DUT, comprising:

a switching network map defining one or more connections within the switching network necessary to implement each of a plurality of electrical paths from an input of the switching network to an output of the switching network, wherein each of the plurality of electrical paths is representative of a connection of one of the testing devices to one of the I/O ports of the DUT;

a controller for (i) receiving one or more commands, wherein each of the commands uniquely specifies an electrical path connecting a particular testing device to a particular I/O port of the DUT, (ii) comparing each of the commands to the switching network map so as to identify a corresponding electrical path through the switching network, and implementing the corresponding electrical path associated with the command through the switching network, and (iii) sequentially implementing the electrical paths corresponding to the one or more commands in a predetermined order.

9. A system according to claim 8, wherein the switching network includes at least two sub-networks electrically coupled so as to form the plurality of electrical paths.

10. A system according to claim 9, wherein said sub-networks include an SCM and a DUT board.

11. (Amended) A system according to claim 8, wherein each of said one or more commands includes a pin name that is (i) descriptive of the path and (ii) related to the DUT.

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